

Electronic Acknowledgement Receipt

EFS ID:	4490056
Application Number:	10597919
International Application Number:	
Confirmation Number:	7894
Title of Invention:	Multilayer substrate with built-in-chip-type electronic component and method for manufacturing the same
First Named Inventor/Applicant Name:	Osamu Chikagawa
Customer Number:	54066
Filer:	Christopher Andrew Bennett/Rebecca Cordeau
Filer Authorized By:	Christopher Andrew Bennett
Attorney Docket Number:	36856.1458
Receipt Date:	19-DEC-2008
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Application Type:	U.S. National Stage under 35 USC 371

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Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	36856-1458-PTO-20081219-US_IDS_Form__SB_08a.pdf	765665 e343754954462d956e1099807455d8fb6cf3d5eb	no	4

Warnings:

Information:

2		36856-1458-PTO-20081219-references-for-ids.pdf	882035 53981e704317e4e468c6d62a07112997678315c4	yes	16			
Multipart Description/PDF files in .zip description								
Document Description		Start	End					
NPL Documents		1	8					
Foreign Reference		9	16					
Warnings:								
Information:								
Total Files Size (in bytes):				1647700				
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